Sound modular verification of code running in an untrusted binary code context

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Introduction

Great progress in sound modular verification of source code

- ... but (except for some rare cases) whole-system verification is not yet reachable
- As a consequence, modularly verified code needs to run side-by-side with unverified (= possibly buggy/malicious) code at run time.

Objective:

Maintaining soundness of modular verification after compilation

Our focus for this talk:

- C-like language
- Security properties expressible in separation logic
- Attacker model = attacker can compromise the machine code of the non-verified modules of the system



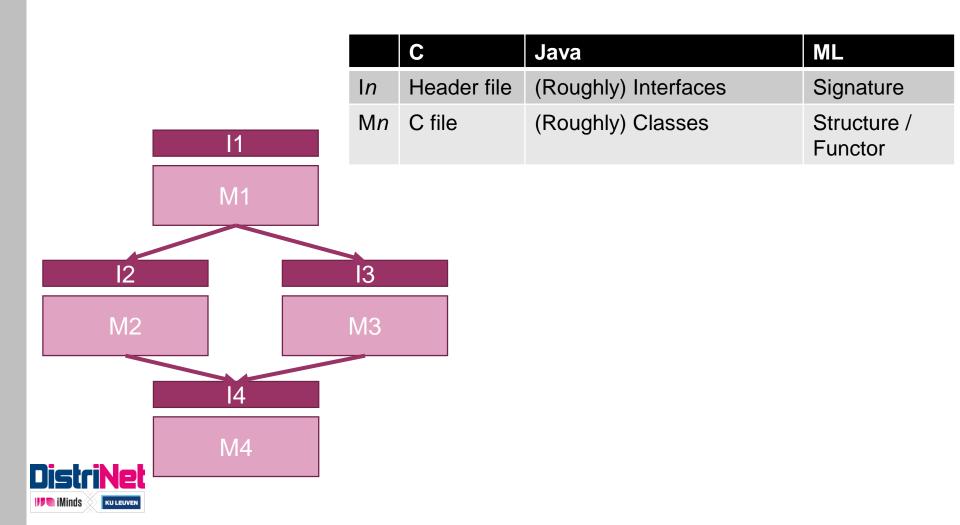
Structure of the talk

- Overview
- Low-level platform protection mechanisms
- Secure compilation of mini-C
- Handling C-style dynamic memory allocation
- Implementation
- Conclusions



Overview

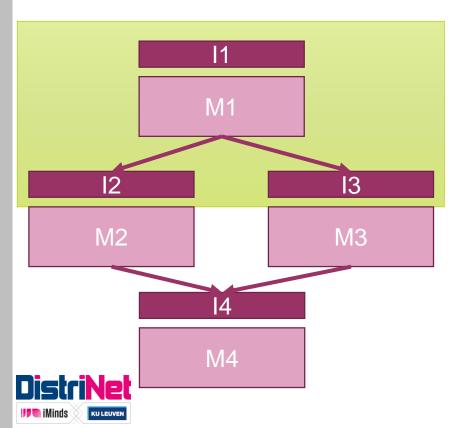
Consider a program consisting of a number of modules, and their dependencies.



Overview

Suppose you have proven a (security) property of module M1 by modular reasoning. E.g.:

- Some invariant holds on the module's state
- The integrity of some data in the module is protected from other modules
- (Some data in the module remains confidential towards other modules)

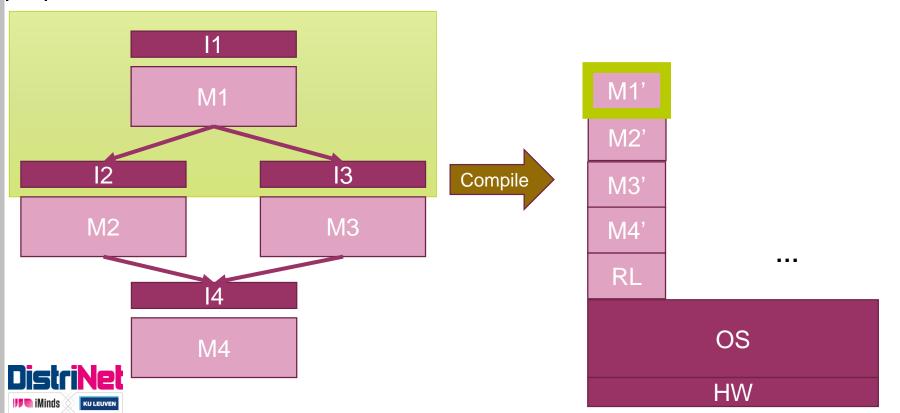


Overview

M1 is compiled to a machine code module M1' running in a process on top of an OS/HW platform.

Other compiled modules and a runtime library (RL) run in the same process and share memory with M1'.

How can we secure interactions between M1' and its context such that verified properties can not be invalidated?



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Low-level protection

- Typed assembly language
 - Morrisett et al. From System F to typed assembly language, ACM TOPLAS (1999)
- Hardware supported low-level security monitors
 - Intel SGX
 - Sancus machine
 - Noorman et al., Sancus: Low-cost trustworthy extensible networked devices with a zero-software trusted computing base, Usenix Security 2013
 - PUMP machine
 - Dhawan et al. Architectural Support for Software-Defined Metadata Processing, ASPLOS 2015



A simplified SGX model

- Standard Intel x86 style platform
 - Processor with
 - Program Counter
 - Registers and a Stack Pointer
 - Status (flags) registers
 - 32-bit memory space mapping 32-bit addresses to 32-bit words
- Extended with a program-counter based memory access control model
 - "SGX enclaves" or "protected modules"
- (Note that SGX has many more features that we do not model)

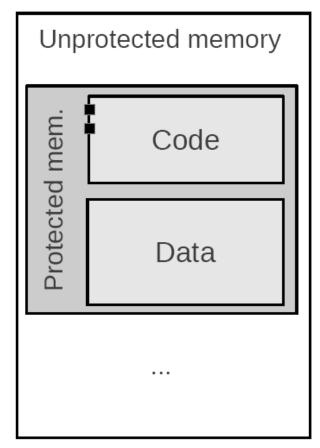


Low-level protection mechanism

- Need some low-level protection mechanism
- Program counter-based memory access control

from \ to	Protected			Unprotected
	Entry point	Code	Data	
Protected	rx	rx	rw	rwx
Unprotected	Х			rwx

0x000000



0xFFFFFF



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Preserving validity of assertions

 Consider a sequential subset of C without dynamic memory allocation

```
M<sub>1.c</sub>
        static int value = 0;
        int get() {
         return value;
        void inc() {
         int oldval = value;
         value += 1;
         int newval = value;
         observer();
         assert(newval == oldval + 1);
          return.
```

```
M2.c void observer() {
   // code of observer omitted
}
```



Preserving validity of assertions

 Consider a sequential subset of C without dynamic memory allocation

```
M<sub>1.c</sub>
        static int value = 0;
        int get() {
         return value;
        void inc() {
         int oldval = value:
         value += 1;
         int newval = value;
         observer();
         assert(newval == oldval + 1);
          return.
```

```
M2.c void observer() {
    // code of observer omitted
    }
```

This assertion is valid according to the source code semantics ...
But fails if an attacker can mess with the machine code of M2.c after compilation.



Standard compilation

```
void observer() {
// code of observer omitted
}
```

```
static int value = 0;
int get() {
 return value;
void inc() {
 int oldval = value;
 value += 1;
 int newval = value;
 observer();
 assert(newval == oldval + 1);
```

Memory Call stack: AR observer() newval AR inc() oldval Return address Static data Machine code for M2 Machine code for M1



Compilation to simplified SGX

- C modules are compiled to SGX enclaves
 - Space for static (private) vars in the data section
 - Machine code for all functions in the text section
 - Entry points for each publicly accessible function
- Calling conventions and call stack:
 - Pass parameters through processor registers
 - Call stack: activation record of a function call stored in the data section of the enclave containing the function
 - A specific return entry point supports returning from a callback



Secure compilation

```
void observer() {
// code of observer omitted
}
```

```
static int value = 0;
int get() {
 return value;
void inc() {
 int oldval = value;
 value += 1;
 int newval = value;
 observer();
 assert(newval == oldval + 1);
```

AR observer()

Static data M2

Machine code
for M2

AR inc()

newval
oldval
Return address
Static data M1

Machine code
for M1

Enclave for M1

Enclave for M2

Secure compilation

Many details to get right

- Depending on the source language features to support
 - Function pointers, objects, classes, exceptions, ...

See the following papers:

- Agten et al. Secure compilation to modern processors, CSF 2012
- Patrignani et al. Secure compilation to protected module architectures, TOPLAS 2015

for formal proofs that this style of compilation can be made **fully abstract**

 i.e. machine code can only interact with a module as source code can

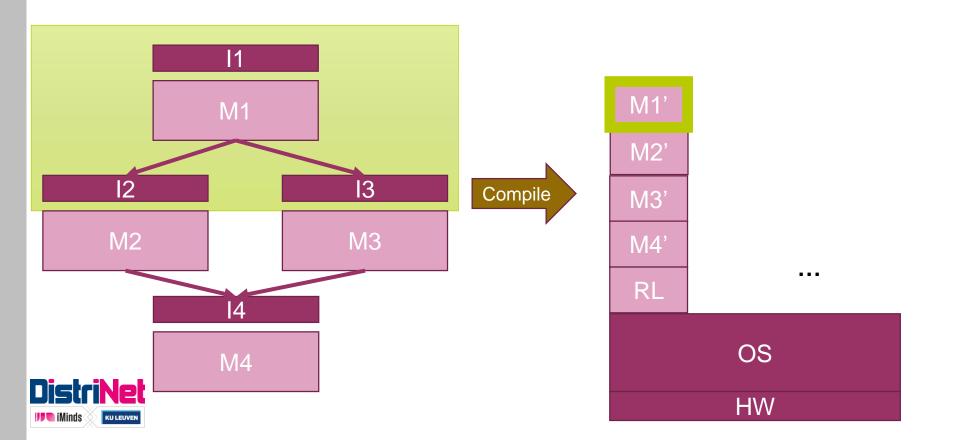


What about interface specs?

We now know how to compile M1 to a hardened M1' such that machine code contexts can only do what source code contexts can do.

But verification of properties of M1 might rely on specs of I2 and I3!

HENCE: we need to insert run time checks for these contracts.



- We will implement interface contract checking as a source-to-source program transformation
- The resulting "hardened" module can be verified in any source code context
 - i.e. with empty interface contracts
- And hence will maintain any verified property after compilation to machine code with the secure (fully abstract) compiler just discussed



```
// Original module
int fac(int x)
  req x >= 0;
  ens res == fact(x);
{
   if (x == 0) return 1;
   int p = prod(x,
      fac(x-1));
   return p;
}
int prod(int x, int y);
  req true;
  ens res == x * y;
```

Program transformation

```
// Hardened module
// (Functional part)
static int _fac(int x) {
  if (x == 0) return 1;
  int p = \_prod(x,
    _{\text{fac}}(x-1);
  return p;
// (Boundary part)
static
int _prod(int x, int y){
  int r = prod(x, y);
  if (! (r == x * y))
    trap();
  return r;
int fac(int x) {
  if (! (x >= 0)) trap();
  return _fac(x);
```

```
// Original module
int fac(int x)
  req x >= 0;
  ens res == fact(x);

{
   if (x == 0) return 1;
   int p = prod(x,
     fac(x-1));
   return p;
}
```

```
int prod(int x, int y);
req true;
ens res == x * y;
```

Program transformation

Alpha-rename the body of the verified functions

```
// Hardened module
// (Functional part)

static int _fac(int x) {
   if (x == 0) return 1;
   int p = _prod(x,
        _fac(x-1));
   return p;
}
```

```
// (Boundary part)
static
int _prod(int x, int y){
   int r = prod(x, y);
   if (! (r == x * y))
        trap();
   return r;
}

int fac(int x) {
   if (! (x >= 0)) trap();
   return _fac(x);
}
```



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int fac(int x)
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   if (x == 0) return 1;
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   return p;
}

int prod(int x, int y);
  req true;
  ens res == x * y;
```

Program transformation

On entry, check validity of the precondition

```
// Hardened module
// (Functional part)
static int _fac(int x) {
  if (x == 0) return 1;
  int p = \_prod(x,
    _{\text{fac}}(x-1);
  return p;
// (Boundary part)
static
int _prod(int x, int y){
  int r = prod(x, y);
  if (! (r == x * y))
    trap();
  return r;
int fac(int x) {
```

```
int fac(int x) {
   if (! (x >= 0)) trap();
   return _fac(x);
}
```



```
// Original module
int fac(int x)
  req x >= 0;
  ens res == fact(x);
{
   if (x == 0) return 1;
   int p = prod(x,
      fac(x-1));
   return p;
}
```

```
int prod(int x, int y);
  req true;
  ens res == x * y;
```

Program transformation

On outcall, check validity of the postcondition

```
// Hardened module
// (Functional part)
static int _fac(int x) {
  if (x == 0) return 1;
  int p = \_prod(x,
    _{\text{fac}}(x-1);
  return p;
// (Boundary part)
static
int _prod(int x, int y){
  int r = prod(x, y);
  if (! (r == x * y))
    trap();
  return r;
int fac(int x) {
  if (! (x >= 0)) trap();
  return _fac(x);
```

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```
// Prototypes
Context
               int med(struct lst *1);
               // Unverified functions
               int main()
                 struct 1st *1 =
                  input_list();
                 output_int(med(1));
               void srt(struct 1st *1)
                < unverified sort
                   implementation >
```

```
// Prototypes
                            Module
void srt(struct 1st *1);
// Verified functions
int med(struct 1st *1)
int s = len(1);
 struct 1st *10 =
  copy(1);
 srt(10);
 return nth(10, s/2);
```



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// Prototypes
Context
               int med(struct 1st *1);
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               int main()
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               int main()
                 struct 1st *1, =
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// Prototypes
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void srt(struct 1st *1);
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int med(struct 1st *1)
int 8 = 1en(1);
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cproof statements>
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```



Extending the interface checks

- Before, interface checks were just assertion checks:
 - On entering a function of the module
 - On returning from an outcall
- Now, these assertions can be "spatial" assertions:
 - Interface checks should also maintain the footprint of the module
 - And check integrity of that footprint
 - On entering a function of the module
 - On returning from an outcall



```
struct pair {int a, b;};
void f(struct pair* p)
 req p->a |->?a &*&
     p->b |->?b
 ens p->a |-> _ &*&
    p->b |-> _:
 <....>
 ct(p);
 <....>
void ct(struct pair* p);
 req p\rightarrowa \mid ->?n;
 ens p->a |-> ?m &*&
     m == n + 1;
```

// Original module

Program transformation

```
// Hardened module
struct pair {int a, b;};
static
void _f(struct pair* p) {
 <....>
 _ct(p);
 <....>
static
void _ct(struct pair* p) {
 char h0[32], h1[32];
 int n = intp(\&(p->a),C);
 fp_hash(h0);
 ct(p);
 fp_hash(h1);
 if (!eq(h0, h1)) trap();
 int m = intp(\&(p->a), P);
 if (m != n+1) trap();
void f(struct pair* p) {
 a = intp(\&(p->a),P);
 b = intp(&(p->b),P);
 _f (p);
 intp(&(p\rightarrow a),C);
 intp(&(p->b),C);
                             46
```

```
// Original module
struct pair {int a, b;};
void f(struct pair* p)
 req p->a |-> ?a &*&
     p->b |-> ?b
 ens p->a |-> _ &*&
     p->b |-> _:
 <....>
 ct(p);
 <....>
void ct(struct pair* p);
 req p\rightarrowa \mid ->?n;
 ens p->a |-> ?m &*&
     m == n + 1;
```

Program transformation

On entry:

Produce the footprint of the precondition

```
// Hardened module
struct pair {int a, b;};
static
void _f(struct pair* p) {
 <....>
 _ct(p);
 <....>
static
void _ct(struct pair* p) {
 char h0[32], h1[32];
 int n = intp(\&(p->a),C);
 fp_hash(h0);
 ct(p);
 fp_hash(h1);
 if (!eq(h0, h1)) trap();
 int m = intp(\&(p->a), P);
 if (m != n+1) trap();
void f(struct pair* p) {
 a = intp(\&(p\rightarrow a), P);
 b = intp(\&(p->b), P);
 _f (p);
 intp(&(p\rightarrow a),C);
 intp(&(p->b),C);
                              47
```

```
// Original module
struct pair {int a, b;};
void f(struct pair* p)
 req p->a |->?a &*&
     p - > b | - > ?b
 ens p->a |-> _ &*&
     p->b |-> _;
 <....>
 ct(p);
 <....>
void ct(struct pair* p);
 req p\rightarrowa \mid ->?n;
 ens p->a |-> ?m &*&
     m == n + 1;
```

Program transformation

On return:

Consume the footprint of the postcondition

```
// Hardened module
struct pair {int a, b;};
static
void _f(struct pair* p) {
 <....>
 _{ct(p)};
 <....>
static
void _ct(struct pair* p) {
 char h0[32], h1[32];
 int n = intp(\&(p->a),C);
 fp_hash(h0);
 ct(p);
 fp_hash(h1);
 if (!eq(h0, h1)) trap();
 int m = intp(\&(p\rightarrow a), P);
 if (m != n+1) trap();
void f(struct pair* p) {
 a = intp(\&(p->a),P);
 b = intp(&(p->b),P);
 _{\mathbf{f}}(\mathbf{p});
 intp(&(p\rightarrow a),C);
 intp(&(p\rightarrow b),C);
```

```
// Original module
struct pair {int a, b;};

void f(struct pair* p)
req p->a |-> ?a &*&
    p->b |-> ?b
ens p->a |-> _ &*&
    p->b |-> _;

{
    <...>
    ct(p);
    <...>
}
```

```
void ct(struct pair* p);

req p->a |-> ?n;

ens p->a |-> ?m &*&

m == n + 1;
```

Program transformation

On outcall:

Consume the footprint of the precondition

```
// Hardened module
struct pair {int a, b;};
static
void _f(struct pair* p) {
 <....>
 _ct(p);
 <....>
static
void _ct(struct pair* p) {
 char h0[32], h1[32];
 int n = intp(&(p->a),C);
 fp_hash(h0);
 ct(p);
 fp_hash(h1);
 if (!eq(h0, h1)) trap();
 int m = intp(\&(p\rightarrow a), P);
 if (m != n+1) trap();
void f(struct pair* p) {
 a = intp(\&(p->a),P);
 b = intp(&(p->b),P);
 _f(p);
 intp(&(p\rightarrow a),C);
 intp(&(p->b),C);
```

```
struct pair {int a, b;};
void f(struct pair* p)
 req p->a |->?a &*&
     p - > b | - > ?b
 ens p->a |-> _ &*&
    p->b |-> _:
 <....>
 ct(p);
 <....>
void ct(struct pair* p);
 req p\rightarrowa \mid ->?n;
 ens p->a |-> ?m &*&
     m == n + 1;
```

// Original module

Program transformation

On return from outcall:

Produce the footprint of the postcondition

```
// Hardened module
struct pair {int a, b;};
static
void _f(struct pair* p) {
 <....>
 _ct(p);
 <....>
static
void _ct(struct pair* p) {
 char h0[32], h1[32];
 int n = intp(\&(p->a),C);
 fp_hash(h0);
 ct(p);
 fp_hash(h1);
 if (!eq(h0, h1)) trap();
 int m = intp(\&(p\rightarrow a), P);
 if (m != n+1) trap();
void f(struct pair* p) {
 a = intp(\&(p->a),P);
 b = intp(&(p->b),P);
 _f(p);
 intp(&(p\rightarrow a),C);
 intp(&(p->b),C);
```

```
// Original module
struct pair {int a, b;};
void f(struct pair* p)
 req p->a |->?a &*&
     p - > b | - > ?b
 ens p->a |-> _ &*&
    p->b |-> _:
 <....>
 ct(p);
 <....>
void ct(struct pair* p);
 req p\rightarrowa \mid ->?n;
 ens p->a |-> ?m &*&
     m == n + 1;
```

Frame rule for ct

Program transformation

And check that the context did not modify the contents of the footprint of the module

```
// Hardened module
struct pair {int a, b;};
static
void _f(struct pair* p) {
 <....>
 _ct(p);
 <....>
static
void _ct(struct pair* p) {
 char h0[32], h1[32];
 int n = intp(\&(p->a),C);
 fp_hash(h0);
 ct(p);
 fp_hash(h1);
 if (!eq(h0, h1)) trap();
 int m = intp(&(p->a), P);
 if (m != n+1) trap();
void f(struct pair* p) {
 a = intp(\&(p->a),P);
 b = intp(&(p->b),P);
 _f(p);
 intp(&(p\rightarrow a),C);
 intp(&(p->b),C);
                             51
```

Main theorems

Theorem 2 (Safety). For a command c and well-formed assertions a_{pre} and a_{post} such that $\Gamma \vdash \{a_{pre}\}\ c\ \{a_{post}\}$, we have $\forall \Delta$. nofail(Δ) $\vDash \{\top\}\ [c]_{\Gamma,a_{pre}}\ \{\top\}$.

Theorem 3 (Precision). For a command c and well-formed assertions a_{pre} and a_{post} such that $\Gamma \vdash \{a_{pre}\}\ c\ \{a_{post}\}$, we have that $\forall \Delta$. $\Delta \vDash \Gamma \Rightarrow \Delta \vDash \{a_{pre}\}\ [c]_{\Gamma,a_{pre}}\ \{a_{post}\}$.

For details, see: Agten et al. Sound modular verification of C code executing in an unverified context, POPL 2015



Structure of the talk

- Overview
- Low-level platform protection mechanisms
- Secure compilation of mini-C
- Handling C-style dynamic memory allocation
- Implementation
- Conclusions



Implementation

- We have an end-to-end implementation
 - Verifier = VeriFast [very mature]
 - Program transformations for run time contract checking support a subset of C and VeriFasts program logic [prototype]
 - Secure compiler is an LLVM based "pragmatic" implementation of a fully abstract compiler [initial prototype]
 - Protected Module Architecture is either Sancus or Fides [stable prototypes], but should be Intel SGX soon

Benchmarks show acceptable costs

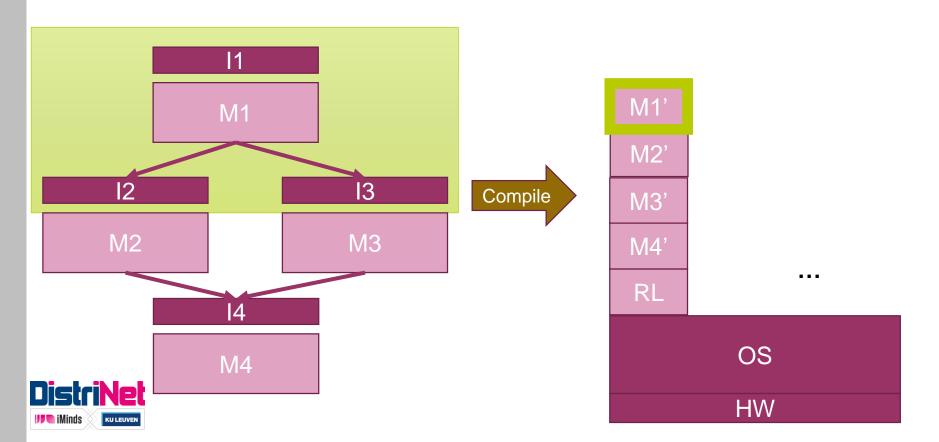
	Execution time (s)		
	unhardened	hardened	overhead
mod_authn_anon	33.164	33.388	0.224~(0.68)%
mod_authn_file	33.554	34.809	1.255 (3.74)%
ftpd	23.193	23.242	0.049~(0.21)%



Conclusions

A property verified of M1 is true at run time, relying only on:

- Soundness of the verifier
- Correctness and security of the compiler (including the runtime checks discussed in this talk)
- Correctness of the hardware (including the memory access control)



Future Work

- Implementation and benchmarking on Intel SGX
- Handling concurrency
 - VeriFast is sound for concurrent code
 - But clearly, the current run time contract checks are not
- Preserving relational program properties
 - For instance non-interference
 - The current run time checks are only sound for safety properties
- Evaluating other low-level protection mechanisms
-



Questions?

Thank you!



References

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